

DVCon U.S. 2021



Welcome Message from DVCon U.S. General Chair, Vanessa Cooper General Chair – Vanessa Cooper, Verilab, Inc.

I am pleased to welcome you to our virtual DVCon U.S. 2022 conference and exhibition! This year, DVCon will offer attendees a combination of recorded presentations and live Q&A to provide an interactive, high-quality virtual experience. Attendees can look forward to outstanding technical sessions and virtual discussions on many hot topics, as well as networking during the virtual breakout sessions and opportunities to preview the latest industry design and verification tools and services from the best in the industry.

We are proud of continuing our tradition of providing an annual technical forum that serves the needs of the practicing design and verification community, organized by dedicated volunteers from the community itself.

Now in its 34th year, DVCon U.S. has established itself as the must-attend industry and user-focused conference for design and verification engineers, EDA developers, IP integrators and design managers, focusing on design and verification of electronic systems and integrated circuits. We are proud that this conference attracts wide participation from the industry from the smaller to the larger companies throughout the program and exhibition.

Our four-day virtual program contains many key design and verification topics including RISC-V, cloud-based design, open source, formal verification, portable stimulus, IP security, UVM, functional safety, prototyping and emulation, SystemC, and many others. The event provides an opportunity to discuss challenges and solutions that can be beneficial in current and upcoming projects as electronic designs and verification complexities and challenges continue to increase exponentially. Attendees will find each of these areas addressed at the conference's sessions, panels, posters, tutorials, and short workshops with an emphasis on solutions to engineers' real-world problems.

We are pleased to offer attendees an in-depth technical program with a wide variety of choices. We received numerous outstanding submissions for papers, panels, tutorials, and short workshops from the best technical minds and organizations in the industry. Our focus on the users of Accellera standard EDA languages, tools, and methodologies continues to be a DVCon 2022 hallmark. Attendees can expect to learn about both practical solutions to their pressing problems and preview the technologies that will affect them in the near future.

One of the benefits of a virtual conference is the on-demand availability of the sessions. With such an extensive program, full-conference attendees will have access to the entire program and will not have to choose between parallel sessions. They can enjoy the recorded sessions at their leisure, and from anywhere in the world.

I am pleased to present the work of the DVCon Steering Committee and Technical Program Committee, who have put together an excellent 2022 program with the support of our conference management specialists, Conference Catalysts.

Highlights of the conference include:

Accellera Day: On Monday, February 28, our conference sponsor, Accellera Systems Initiative, kicks off DVCon U.S. with Accellera Day. We will have a morning Accellera tutorial on Portable Stimulus focused real-world examples, as well as three Accellera short workshops presented by Accellera standards working group members. We will have four sponsored short workshops in the afternoon presented by design verification industry members covering topics such as RISC-V verification, emulation, power dissipation estimation, and co-verification.

Keynote: This year's keynote, "Unleashing AI/ML for Faster Verification Closure," will be given by Dr. Manish Pandey, Vice President R&D and Fellow at Synopsys, Inc.. In his presentation Dr. Pandey will discuss how the advances of utilizing Machine Learning algorithms have enabled significant gains in the verification flow.

Technical papers and posters: Technical Program Chair, John Dickol, and Poster Chair, Xiaolin Chen, have organized an excellent technical program on Tuesday and Wednesday that includes 42 papers and 14 posters. We are very grateful for the outstanding submissions and the work done by the technical program committee volunteers to review the submissions and encourage the community to keep submitting. There are so many good choices that you will want to go through the program and review it thoroughly as you plan each day. There is something for everyone in this broad, in-depth technical program. With so many interesting options, we look forward to your votes for the best paper and best poster awards after the last program session on Wednesday.

Tutorials and Short Workshops: Tom Fitzpatrick, Tutorial and Short Workshop Chair, has put together an outstanding selection of tutorials and short workshops for Monday and Thursday. The short workshops are extremely popular and are intended to give more organizations, mostly smaller companies, greater opportunity to participate in the program and give attendees more variety in shorter educational and learning sessions. We have 13 sponsored short workshops in this year's program—seven on Monday and six on Thursday—covering a wide variety of topics.

We will have three sponsored tutorials on Thursday with topics covering: Verification Strategy and Methodology, Security, and Machine Learning.

Panels: Ambar Sarkar, Panel Chair, has organized two interesting and thought-provoking panel sessions: "The Meeting of the Soc Verification Hidden Dragons," and "Going Faster – How to Cope with Shrinking Schedules and Increasing Complexity." Both panels will offer attendees an opportunity to ask questions during a live Q&A on the conference platform.

Virtual Exhibition: We are in the process of creating an interactive expo that will be a fun gathering place for attendees to connect with colleagues as well as learn about the latest products in the design and verification industry. Attendees can visit our exhibitors using the virtual space called Gather that will be integrated into the platform.

My sincere thanks to our program sponsor, Accellera Systems Initiative, industry sponsors, steering committee volunteers, technical program committee volunteers, past chairs and Conference Catalyst staff who have worked hard to put together a program that makes DVCon "the" conference for design and verification engineers.

I sincerely look forward to "seeing" you online at DVCon U.S. 2022!

Vanessa Cooper

DVCon U.S. 2022 General Chair